

GAU 2751  
\$ # 7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Mailloux, et al.

Serial No.: 08650,719

Filed: May 20, 1996

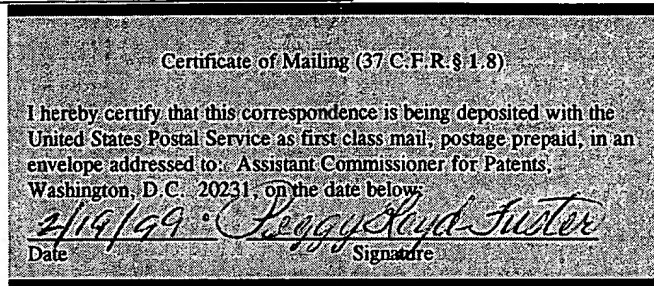
For: BURST/PIPELINED EDO  
MEMORY DEVICE

§  
§ Group Art Unit: 2751  
§  
§ Examiner: Kim, H.  
§  
§ Atty. Docket: 95-0653.00  
§  
§  
§  
§  
§  
§

RECEIVED  
MAR 02 1999  
Group 2700

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231



Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant[s] respectfully request[s] that this Information Disclosure Statement be entered and that the references listed on the attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. § 1.97(b), this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possible material information as defined in 37 C.F.R. § 1.56(a) exists.

The following references are submitted for the Examiner's review:

U.S. Patents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
5,652,724	7/97	Manning
5,457,659	10/95	Schaefer
5,452,261	9/95	Chung et al.

5,682,354	10/97	Manning
5,640,364	06/97	Merritt et al.
5,729,504	12/95	Cowles
5,661,695	08/97	Zagar et al.
5,305,284	04/94	Iwase
5,325,330	06/94	Morgan
5,325,502	06/94	McLaury
5,373,227	12/94	Keeth
5,410,670	04/95	Hansen et al.
5,349,566	09/94	Merritt et al.
5,668,773	09/97	Zagar et al.
4,870,622	09/89	Aria et al.
5,058,066	10/91	Yu
5,280,594	01/94	Young et al.

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**MAR 02 1999**

**Group 2700**

Other References

Micron Technology, Inc. , "1995 DRAM Data Book" pp. 4-1 thru 4-42, 12/95.

"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories, entire book.

Related Co-pending Applications

PCT Patent Application No. PCT/US95/16656, filed 12/21/1995, entitled: Burst Edo Memory Device

Address Counter.

PCT Patent Application No. PCT/US95/16984 , filed 12/22/1995, entitled: Burst Edo Memory Device.

This Information disclosure Statement is being submitted after the mailing of the first Office Action, but before the mailing of a Final Rejection or Notice of Allowance. The Commissioner is authorized to charge the fee set forth in 37 C.F.R. § 1.17(p) of \$240.00 and any additional fees which may be required to Micron Technology, Inc. Deposit Account No. 13-3092, Order No. 95-0653.00.

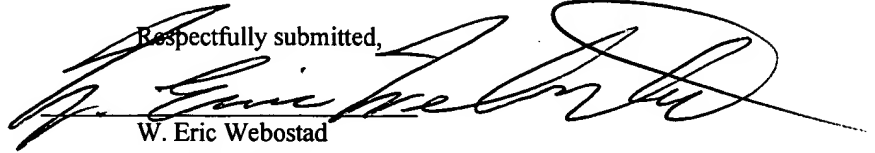
If there are any matters which may be resolved or clarified through telephone interview, the Examiner is respectfully requested to contact Applicant's undersigned attorney at the number indicated.

\* \* \* \*

A Form PTO-1449 is enclosed herewith.

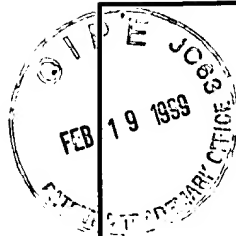
Date: 2-18-99

Respectfully submitted,



W. Eric Webostad  
Reg. No. 35,406  
Micron Technology, Inc.  
8000 S. Federal Way  
Boise, ID 83706-9632  
(208) 368-4792

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



## FEE TRANSMITTAL

## Complete if Known

Application Number	08/650,719
Filing Date	May 20, 1996
First Named Inventor	Mailloux, et al.
Group Art Unit	2751
Examiner Name	Kim, H.
Attorney Docket Number	95-0653

TOTAL AMOUNT OF PAYMENT (\$ ) 240

## METHOD OF PAYMENT (check one)

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any over payments to:

Deposit Account Number 13-3092 /95-0653

Deposit Account Name MICRON TECHNOLOGY, INC.

- ☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17
- ☐ Charge the Issue Fee Set in 37 CFR 1.18 at the Mailing of the Notice of Allowance, 37 CFR 1.31(b)

2. ☐ Payment Enclosed:
- ☐ Check ☐ Money Order ☐ Other

## FEE CALCULATION (fees effective 10/01/96)

## 1. FILING FEE

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
101	790	201	395	Utility filing fee	
106	330	206	165	Design filing fee	
107	540	207	270	Plant filing fee	
108	790	208	395	Reissue filing fee	
114	150	214	75	Provisional filing fee	
SUBTOTAL (1)					(\$)-0-

## 2. CLAIMS

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
103	22	203	11	Claims in excess of 20	
102	82	202	41	Independent claims in excess of 3	
104	270	204	135	Multiple dependent claim	
109	82	209	41	Reissue independent claims over original patent	
110	22	210	11	Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)					(\$)-0-

## FEE CALCULATION (continued)

## 3. ADDITIONAL FEES

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing or cover sheet	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for response within first month	
116	400	216	200	Extension for response within second month	
117	950	217	475	Extension for response within third month	
118	1,570	218	755	Extension for response within fourth month	
119	310	219	155	Notice of Appeal	
120	310	220	155	Filing a brief in support of an appeal	
121	270	221	135	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive unavoidably abandoned application	
141	1,320	241	660	Petition to revive unintentionally abandoned application	
142	1,320	242	660	Utility issue fee (or reissue)	
143	450	243	225	Design issue fee	
144	670	244	335	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Petitions related to provisional applications	
126	240	126	240	Submission of Information Disclosure Stmt	240
581	40	581	40	Recording each patent assignment per property (times number of properties)	
146	790	246	395	Filing a submission after final rejection (37 CFR 1.129(a))	
149	790	249	395	For each additional invention to be examined (37 CFR 1.129(b))	
SUBTOTAL (3)					(\$) 240

Other fee (specify) \_\_\_\_\_

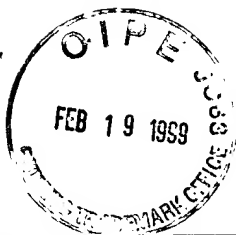
Other fee (specify) \_\_\_\_\_

\* Reduced by Basic Filing Fee Paid

## SUBMITTED BY

Complete (if applicable)

Typed or Printed Name	W. Eric Wobstad	Reg. Number	35,406
Signature		Date	2-19-99
		Deposit Acct. User ID	13-3092/95-0653



Sheet: 1 of: 2

FORM: PTO-1449 (REV: 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Atty Docket No: <b>95-0653</b>	Serial No: <b>08/650,719</b>
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (37 CFR 1.98(b)) (use several sheets if necessary)		Applicant: <b>Mailloux et al.</b>	
		Filing Date: <b>5/20/96</b>	Group: <b>2751</b>

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	
	AA	5,652,724	7/97	Manning	365	189.05
	AB	5,457,659	10/95	Schaefer	365	222
	AC	5,452,261	9/95	Chung et al.	265	233
	AD	5,280,594	1/94	Young et al.	395 711	425 157
	AE	5,058,066	10/91	Yu	365	189.05
	AF	4,870,622	9/89	Aria et al.	365	230.02
	AG	5,668,773	09/97	Zagar et al.	365	233
	AH	5,325,502	06/94	McLaury	395 711	425 169
	AI	5,373,227	12/94	Keeth	323	313
	AJ	5,410,670	04/95	Hansen et al.	395 711	425 169
	AK	5,349,566	09/94	Merritt et al.	365	233.5

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
	AL					<input type="checkbox"/>	<input type="checkbox"/>
	AM					<input type="checkbox"/>	<input type="checkbox"/>
	AN					<input type="checkbox"/>	<input type="checkbox"/>
	AO					<input type="checkbox"/>	<input type="checkbox"/>
	AP					<input type="checkbox"/>	<input type="checkbox"/>
	AQ					<input type="checkbox"/>	<input type="checkbox"/>

Initial

**OTHER ART** (including author, title, date, pertinent pages, etc.)

	AR		Micron Technology, Inc., "1995 DRAM Data Book" pp. 4-1 thru 4-42, 12/95
			"Rossini, Pentium, PCI-ISA, Chip Set", Symphony Laboratories, entire book.
			Date
	AT		PCT Patent Application No. PCT/US95/16984, filed 12/22/1995, entitled: Burst Edo Memory Device.

Examiner:	Date Considered:
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.

FORM: PTO-1449 (REV: 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Atty Docket No: <b>95-0653</b>	Serial No: <b>08/650,719</b>
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (37 CFR 1.98(b)) (use several sheets if necessary)		Applicant: <b>Mailloux et al.</b>	
		Filing Date: <b>5/28/96</b>	Group: <b>2751</b>

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	
	AA	5,682,354	10/97	Manning	365	233.5
	AB	5,640,364	06/97	Merritt et al.	365	233.5
	AC	5,729,504	12/95	Cowles	365	236
	AD	5,661,695	08/97	Zagar et al.	365	233.5
	AE	5,305,284	04/94	Iwase	365	238.5
	AF	5,325,330	06/94	Morgan	365	189.05
	AG					
	AH					
	AI					
	AJ					
	AK					

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes	Translation No
	AL					<input type="checkbox"/>	<input type="checkbox"/>
	AM					<input type="checkbox"/>	<input type="checkbox"/>
	AN					<input type="checkbox"/>	<input type="checkbox"/>
	AO					<input type="checkbox"/>	<input type="checkbox"/>
	AP					<input type="checkbox"/>	<input type="checkbox"/>
	AQ					<input type="checkbox"/>	<input type="checkbox"/>

Initial

OTHER ART (including author, title, date, pertinent pages, etc.)

			PCT Patent Application No. PCT/US95/16656, filed 12/21/1995, entitled: Burst Edo Memory Device Address Counter.
	AS		
	AT		

Examiner:	Date Considered:
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.